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APPLICANTS

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** CONTINUING DATA *** *Verify* ***** *PL*

This application is a CON of 10/410,390 04/08/2003 PAT 6,714,431
which is a CON of 10/045,864 01/09/2002 PAT 6,583,035
which is a DIV of 09/471,305 12/23/1999 PAT 6,404,660

** FOREIGN APPLICATIONS *** *None* ***** *PL*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/01/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	Examiner's Signature _____ Initials _____	CA	19	1	1

ADDRESS

38426
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TITLE

Semiconductor package with a controlled impedance bus and method of forming same

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